IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No	08/728,066
Priority Filing Date	10/9/1996
Inventorship	Feinleib et al.
Priority Examiner	Dang Ton
Priority Group Art Unit	2732
Attorney's Docket No	MS1-108USC1
Priority Filing Date	
Over Satellite Network	_

<u>INFORMATION DISCLOSURE STATEMENT</u>

The citations listed are submitted in compliance with the duty of disclosure defined in 37 CFR §1.56. Copies of the cited references were cited or submitted with the priority application and are therefore not submitted herewith.

The Examiner is requested to make these citations of official record in this application.

Respectfully Submitted,

Reg. No. 39,241

Date: 1-5-2001

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

ATTY. DOCKET NO. MS1-108USC1

SERIAL NO.

APPLICANT Microsoft Corporation

FILING DATE

GROUP

					U.S. PATENT DOCUMENTS				` -			
*Examiner Initial		Document Number 4,962,498 5,157,659		Date	Name	Class	Subclass	Filing Date If Appropriate				
	А			10/90	Max	370	474					
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	D	5,689,501 5,764,645 5,715,250		11/97 Takase et al.		370	397					
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				OTHER PR	IOR ART (including Author, Title, Date, Pertinent Pages, Etc.)							
	E		H. Jonathan Chao et al., "A Shared-Memory Virtual Channel Queue For ATM Broadband Terminal Adaptors", International Journal of Digital and Analog Communication Systems, Vol.5, 29-37 (1992)									
	F		D. Lutas et al., "Multiprocessor system for interconnection of Ethernet and FDDI networks using ATM via satellite", IEE Proc Computer Digit Tech, Vol. 143, No. 1, January 1996, pp.69-78									
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EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.